FIG. 1

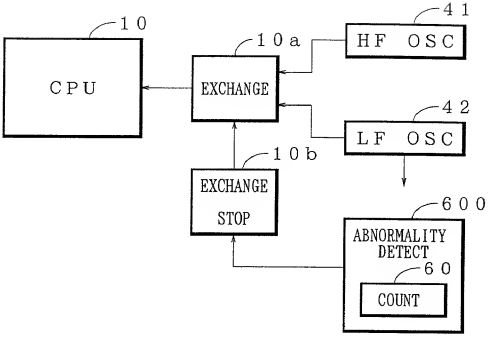

10
CPU
10a
EXCHANGE
41
HF OSC
42
LF OSC
10b
EXCHANGE STOP
600
ABNORMALITY DETECT
60
COUNT

FIG. 2

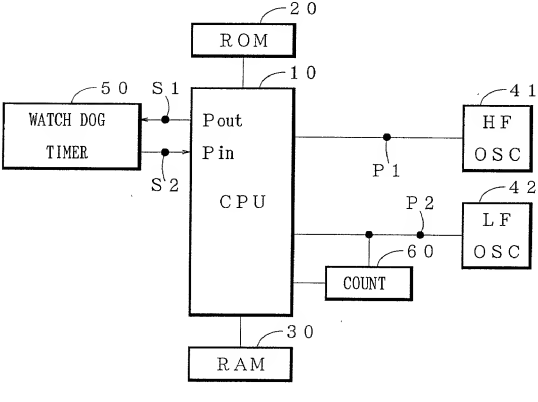
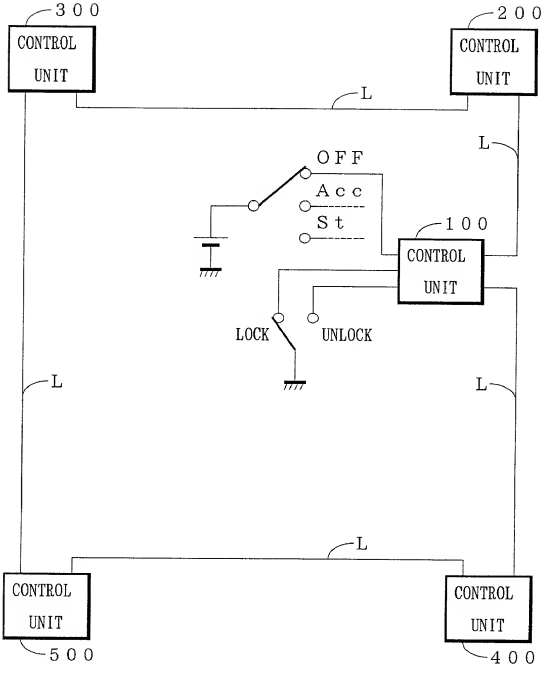

20
ROM
10
50
S1
WATCH DOG TIMER
S2
Pout
Pin
CPU
41
HF OSC
P1
42
LF OSC
P2
60
COUNT
30
RAM

FIG. 3



T00280"T07T4660

FIG. 4

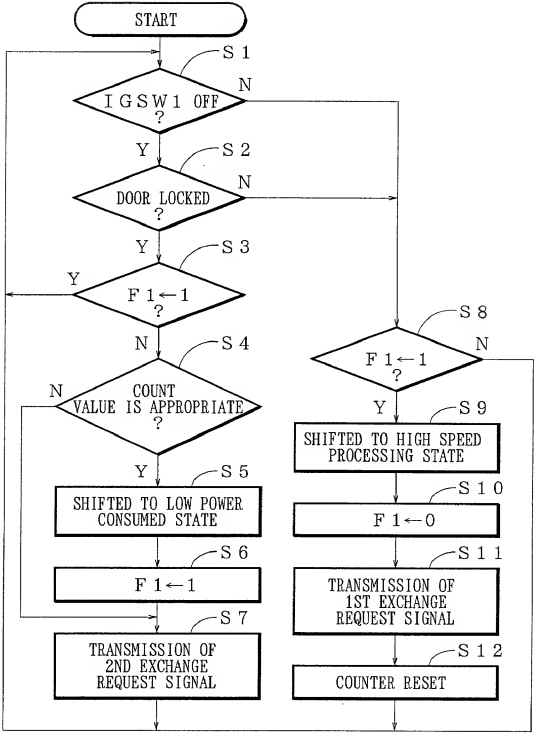

START
S 1
IGSW1 OFF ?
N
Y
S 2
DOOR LOCKED ?
N
Y
S 3
F 1←1 ?
Y
N
S 4
COUNT VALUE IS APPROPRIATE ?
N
Y
S 5
SHIFTED TO LOW POWER CONSUMED STATE
S 6
F 1←1
S 7
TRANSMISSION OF 2ND EXCHANGE REQUEST SIGNAL
S 8
F 1←1 ?
N
Y
S 9
SHIFTED TO HIGH SPEED PROCESSING STATE
S 10
F 1←0
S 11
TRANSMISSION OF 1ST EXCHANGE REQUEST SIGNAL
S 12
COUNTER RESET

FIG. 5

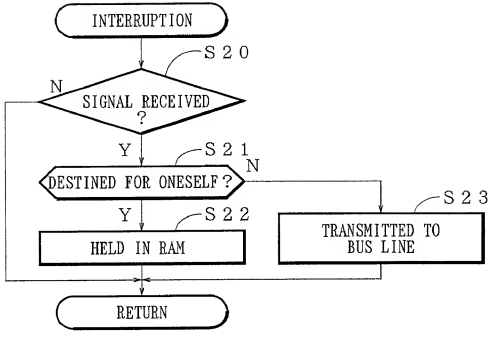

INTERRUPTION
S20
N
SIGNAL RECEIVED ?
Y
S21
DESTINED FOR ONESELF ?
N
Y
S22
HELD IN RAM
S23
TRANSMITTED TO BUS LINE
RETURN

FIG. 7

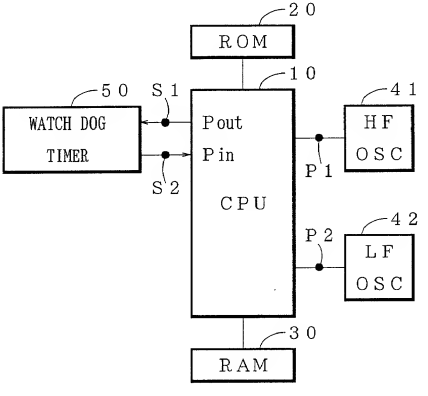
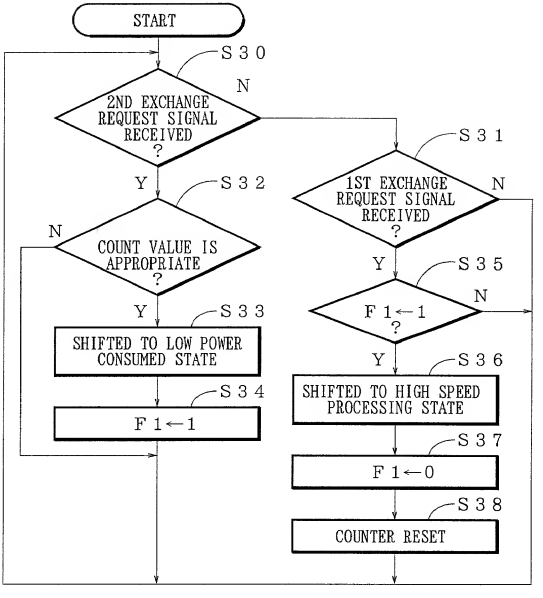

20
ROM
10
50
S1
WATCH DOG TIMER
Pout
Pin
S2
CPU
41
HF OSC
P1
P2
42
LF OSC
30
RAM

FIG. 6


START
S30
2ND EXCHANGE REQUEST SIGNAL RECEIVED ?
N
Y
S32
COUNT VALUE IS APPROPRIATE ?
N
Y
S33
SHIFTED TO LOW POWER CONSUMED STATE
S34
F1←1
S31
1ST EXCHANGE REQUEST SIGNAL RECEIVED ?
N
Y
S35
F1←1 ?
N
Y
S36
SHIFTED TO HIGH SPEED PROCESSING STATE
S37
F1←0
S38
COUNTER RESET